

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:	TI-37400
Sridhar Ramaswamy, et al.	Art Unit: 2863
Serial No: 10/777,238	Examiner: Bryan Bui
Filed: February 12, 2004	Conf. No.: 4500
For: Methodology for Designing High Speed Receivers Below a Target Bit-Error-Rate	

AMENDMENT PURSUANT TO 37 CFR 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed January 8, 2007, please enter the following: